



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jozef C. Mitros, et al.

Art Unit: 2815

Serial No.: 10/725,641

Examiner: Matthew E. Warren

Filed: 12/02/03

Docket: TI-32452

For: MOS Transistors Having Higher Drain Current Without Reduced Breakdown
Voltage

LETTER TO THE OFFICIAL DRAFTSPERSON

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service on 1-10-05 as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Karen Vertz
Karen Vertz

1-10-05
Date

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed are **SIX (6)** sheets of formal drawings for the above-referenced case. Please charge any necessary fees to Deposit Account No. 20-0668 of Texas Instruments Incorporated. This sheet is enclosed in triplicate.

Respectfully submitted,


Gary C. Honeycutt
Reg. No. 20,250

Texas Instruments Incorporated
P.O. Box 655474 M/S 3999
Dallas, Texas 75265